## **EXPEDITED PROCEDURE: - EXAMINING GROUP 2124**

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Dmitriy Rumynin et al.

Examiner: David H. Malzahn

Serial No.:

09/769,954

Group Art Unit: 2124

Filed:

January 25, 2001

Docket No.: 1365.039US1

Title:

A PARALLEL COUNTER AND A MULTIPLICATION LOGIC CIRCUIT

## **AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

RECEIVED

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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**Technology Center 2100** 

In response to the Final Office Action mailed April 28, 2004, please amend the application as follows:

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a two-month extension of the period for responding to the Office action, thereby moving the deadline for response from July 28, 2004 to September 28, 2004.

Per /RCE 11/1/04 C.Sam 11/5/04